

Remarks

In view of the above amendments and the following remarks, reconsideration and further examination are requested.

The specification and abstract have been reviewed and revised to make a number of editorial revisions. Due to the number of changes involved, a substitute specification and abstract have been prepared and are submitted herewith. No new matter has been added. Enclosed is a marked-up copy of the specification and abstract indicating the changes incorporated therein.

Claims 1, 4-7, 22, 25-28, 43 and 44 have been rejected under 35 U.S.C. §102(e) as being anticipated by Dolle (US 6,160,821). Claims 8-14 and 29-35 have been rejected under 35 U.S.C. §103(a) as being unpatentable over Dolle.

Claims 1, 22, 43 and 44 have been amended so as to further distinguish the present invention from the reference relied upon in the rejections. Further, non-elected claims 16-21 and 37-42 have been canceled without prejudice or disclaimer to the subject matter contained therein.

In addition, claims 1-15, 22-35, 43 and 44 have been amended to make a number of editorial revisions. These revisions have been made to place the claims in better U.S. form. None of these amendments have been made to narrow the scope of protection of the claims, nor to address issues related to patentability and therefore, these amendments should not be construed as limiting the scope of equivalents of the claimed features offered by the Doctrine of Equivalents.

The above-mentioned rejections are submitted to be inapplicable to the amended claims for the following reasons.

Claim 1 is patentable over Dolle, since claim 1 recites an OFDM demodulation apparatus for demodulating an OFDM signal which includes a specific synchronous symbol and an OFDM data symbol structured by a valid symbol period and a guard interval, the apparatus having, in part, an impulse response estimation part for estimating an impulse response from the OFDM signal based on the synchronous symbol; an integration part for integrating an output obtained by estimation in the impulse response

estimation part; and a determination part for detecting symbol timing indicating a period where an output obtained by integration in the integration part is maximum. Dolle fails to disclose or suggest an impulse response estimation part, an integration part and a determination part as recited in claim 1.

Dolle discloses a receiver that receives data at a RF-front-end stage 16 and passes the data to an IQ-demodulator 17. The IQ-demodulator 17 generates a complex output signal that is output to a synchronization circuit 18. The synchronization circuit 18 has a timing acquisition circuit 19 and a frequency acquisition circuit 24. The timing acquisition circuit 19 has a delay circuit 30 and a complex conjugation circuit 31 which delay the complex output signal by the duration of one synchronization pattern and complex conjugate the delayed complex output signal. The delayed and complex conjugated signal is then multiplied with the complex output signal by a multiplier 32 to generate mixed data. The mixed data is then fed into a delay circuit 33 and an adder circuit 36. The output signal from the delay circuit 33 is subtracted from the output signal from the adder 36 by a subtraction circuit 34 and the resultant output signal is fed to a delay circuit 35, which delays the resultant output signal and outputs the delayed signal to the adder 36 to be added with the mixed data, thus creating a feedback loop. The output signal from the subtraction circuit 34 is also output to a maximum detection unit 20 that detects the maximum of the output signal. (See column 7, lines 15-56 and Figure 4).

Based on the above discussed elements of the timing acquisition circuit 19, the timing acquisition circuit operates such that the complex output signal is correlated by the combination of the delay circuit 30, the complex conjugation circuit 31 and the multiplier 32. The correlated signal is then summed by the delay circuit 33, the subtraction circuit 34, the delay circuit 35 and the adder circuit 36 in a correlation window (see the dotted rectangles around the reference symbols in phases 1-3 of Fig. 2) to generate a correlation $R(i)$. Then, the maximum detection unit 20 detects the maximum value of the correlation. The reference symbol in Figure 2 is structured by repeating the same synchronization pattern <sp> (see Fig. 1); and as a result, the correlation is maximized by summing the correlation results in a period from a first section to a last section of the reference symbol (see the peak of the triangular portion $R(i)$ in Fig. 2) which is detected

by the maximum detection unit 20. Further, by the maximum detection unit 20 detecting when the correlation is maximized, the end of the reference symbol and the beginning of the guard interval is detected (see phase 2 of Figure 2).

While the rejection indicates that (1) the delay circuit 30, the complex conjugation circuit 31 and the multiplier 32; (2) the delay circuit 33, the subtraction circuit 34, the delay circuit 35 and the adder circuit 36; and (3) the maximum detection unit 20 correspond to (1) the impulse response estimation part; (2) the integration part; and (3) the determination part of the present invention, respectively, it is apparent that these elements of the timing acquisition circuit 19, although operating in a completely different fashion as will be discussed, at best only correspond to the impulse response estimation part. As discussed above, the timing acquisition circuit 19 correlates the complex output signal to a signal obtained by delaying the phase of the complex output signal by a duration of one synchronization pattern. However, the impulse response estimation part estimates an impulse response from an OFDM signal based on a synchronous symbol. Since the impulse response estimation part uses the synchronous symbol to estimate the impulse response, even if a received signal contains a combination of a direct wave and one or more delayed waves due to multipath, it is possible to detect the beginnings of guard intervals of the direct and the one or more delayed waves at a level corresponding to their reception intensities. On the other hand, Dolle relies solely on the maximum detection unit 20 to detect the maximum value of the correlation $R(i)$ to determine the start of the guard interval. Therefore, if one or more delayed waves are received with the direct wave as a combined wave, there is a possibility that the delayed waves could result in the maximum value detected by the maximum detection unit 20 to not actually correspond to the beginning of the guard interval. In other words, Dolle provides no protection against the possibility of multipath. As a result, not only do the delay circuit 30, the complex conjugation circuit 31 and the multiplier 32 fail to correspond the impulse response estimation part, but the timing acquisition circuit 19 as a whole fails to disclose or suggest this feature of claim 1.

Further, based on the above discussion, it is apparent that (1) the combination of the delay circuit 33, the subtraction circuit 34, the delay circuit 35 and the adder circuit 36; and (2) the maximum detection unit 20 fail to disclose or suggest (1) the integration

part for integrating an output obtained by estimation in the impulse response estimation part; and (2) a determination part for detecting symbol timing indicating a period where an output obtained by integration in the integration part is maximum, respectively, as recited in claim 1.


As for claims 22, 43 and 44, these claims are patentable over Dolle for similar reasons as set forth above in support of claim 1. That is, claims 22, 43 and 44, like above claim 1, recite, in part, estimating an impulse response from an OFDM signal based on a synchronous symbol, integrating an output obtained by the estimation, and detecting symbol timing indicating a period where an output obtained by the integration is maximum, which features are not disclosed or suggested in Dolle.

Because of the above mentioned distinctions, it is believed clear that claims 1-15, 22-35, 43 and 44 are allowable over Dolle. Furthermore, it is submitted that the distinctions are such that a person having ordinary skill in the art at the time of invention would not have been motivated to modify Dolle or make any combination of the references of record in such a manner as to result in, or otherwise render obvious, the present invention as recited in claims 1-15, 22-35, 43 and 44. Therefore, it is submitted that claims 1-15, 22-35, 43 and 44 are clearly allowable over the prior art of record.

In view of the above amendments and remarks, it is submitted that the present application is now in condition for allowance. The Examiner is invited to contact the undersigned by telephone if it is felt that there are issues remaining which must be resolved before allowance of the application.

Respectfully submitted,

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TITLE OF THE INVENTION

APPARATUS AND METHOD FOR OFDM DEMODULATION

BACKGROUND OF THE INVENTION

5 Field of the Invention

The present invention relates to apparatuses and methods for OFDM demodulation, and more particularly to an apparatus and a method for demodulating a signal transmitted under an orthogonal frequency division multiplex (OFDM) technique.

10

Description of the Background Art

In recent years, a transmission mode applying the OFDM technique has been popular for digital terrestrial television broadcasting, mobile communications, and the like. The OFDM mode is included in a multicarrier modulation scheme, and carries out OFDM signal transmission from a transmitter to a receiver. The transmitter assigns transmission data to a large number of subcarriers having an orthogonality relation between any adjoining two, and the subcarriers are each modulated by the transmission data assigned thereto. Thereafter, the transmitter collectively subjects the modulated subcarriers to inverse Fourier transform to generate an OFDM signal. In such OFDM signal, the transmission data divided and assigned to the subcarriers may be prolonged in cycle, thereby the OFDM signal is characteristically not-susceptive susceptible to a delay wave, such as multipath.

Such transmission under the OFDM mode is carried out on a transmission symbol basis. The transmission symbol is structured by a valid symbol period and a guard interval (GI). The valid symbol period is a period over which a signal (hereinafter, valid symbol) corresponding to the transmission data is transmitted, and is defined according to the inverse Fourier transform processing. The guard interval is a period over which a signal obtained by partially and cyclically repeating a waveform of the valid symbol is transmitted. The guard interval is for reducing the impact of the delay wave.

The receiver receives the transmission symbol, and extracts the valid symbol therefrom. The receiver then subjects the extracted valid symbol to Fourier transform to separate the valid symbol into the subcarriers. Thereafter, the receiver demodulates each of the separated subcarriers so as to reproduce the transmission data.

The above-described OFDM signal may take such waveform as random noise, therefore it is difficult to establish synchronization in frequency and symbol of the OFDM signal in the receiver. In the case that the OFDM signal is demodulated without frequency synchronization, the orthogonality relation among the subcarriers is lost and thus interference occurs. Further, if the receiver cannot establish symbol synchronization, that is, when the receiver cannot correctly extract the valid symbol from the transmission symbol, interference occurs between the symbols. Accordingly in either case, the receiver fails to correctly reproduce the transmission data.

10 To get around such problem, in the OFDM signal (see FIG. 16) for transmission under the OFDM mode, a symbol (hereinafter, synchronous symbol) being a reference to synchronization is provided at the head of a transmission frame. The transmission frame is structured by several predetermined transmission symbols.

15 The conventional OFDM receiver which establishes symbol synchronization by using such synchronous symbol is found in Japanese Patent Laying-Open No. 11-32025 (99-32025) titled "OFDM receiver and method for detecting synchronization therein". The conventional technique applies a chirp symbol to the synchronous symbol. By calculating a correlation coefficient between the chirp symbol and a received signal, the conventional receiver detects symbol
20 timing from a maximum value thereof, and establishes symbol synchronization therein.

In the case that the multipath ~~is occurred~~ occurs due to a transmission path characteristic, as shown in FIGS. 17A and 17B, the receiver may receive both
25 a direct wave and a delay wave of a transmitting signal, i. e., a combined wave thereof. Herein, when a delay of the delay wave is within the guard interval, the receiver can extract the valid symbol located in a section having no adjacent-symbol interference (FIG. 17A) by following the timing of the valid symbol period of the direct wave. However, this is not applicable to a case where
30 the delay of the delay wave is beyond the guard interval. It may result in extracting the valid symbol having been influenced by the adjacent-symbol interference (FIG. 17B). Therefore, if the adjacent-symbol interference occurs in the receiver, there is a need for setting a section from which the valid symbol is extracted so as to minimize the interference. Note that, the diagonally shaded area in FIGS. 17A and 17B shows a part where the adjacent-symbol
35 interference is observed.

In the conventional receiver, however, the symbol timing is set according to the maximum value of the correlation coefficient between the received signal and the synchronous symbol. Consequently, when the delay of the delay wave is beyond the guard interval, the conventional receiver is incapable of setting the
5 symbol timing in such a manner as to minimize the adjacent-symbol interference.

Further, in the case that the transmitting signal and the received signal have a shift in frequency (hereinafter, frequency shift) therebetween, the correlation coefficient between the received signal and the synchronous symbol becomes smaller. Consequently, the conventional receiver cannot satisfactorily
10 detect the synchronous symbol therein.

Still further, a sampling frequency for sampling symbols may be shifted (hereinafter, sampling frequency shift) between the transmitter and the receiver. If this is the case, the symbol timing setting based on detection of the synchronous symbol as the conventional receiver is not sufficient. It leads to
15 a shift of the symbol timing, i.e., a shift of the valid symbol period between the transmission symbols at the head and at the tail in the transmission frame.

SUMMARY OF THE INVENTION

Therefore, a first object of the present invention is to provide an
20 apparatus for OFDM demodulation capable of setting symbol timing in such a manner as to minimize adjacent-symbol interference even if a transmission path characteristic varies with time, and a method therefor.

A second object of the present invention is to provide an apparatus for OFDM demodulation correcting a frequency shift, if any, for satisfactory
25 synchronous symbol detection, and a method therefor.

A third object of the present invention is to provide an apparatus for OFDM demodulation, if a sampling frequency shift is observed, correcting the symbol timing so as not to cause a shift among valid symbol periods by the location of transmission symbols in a transmission frame, and a method therefor.

30 A fourth object of the present invention is to provide an apparatus for OFDM demodulation, even if the shift is observed among the valid symbol periods by the location of the transmission symbols in the transmission frame, correcting the symbol timing according to information about a transmission path characteristic estimated from a predetermined reference symbol.

35 The present invention has the following features to attain the ~~objects~~ above objects.

A first aspect of the present invention is directed to an OFDM demodulation apparatus for demodulating an OFDM signal which includes a data symbol structured by a valid symbol period and a guard interval, and a specific synchronous symbol is included in the OFDM signal for every transmission frame
5 and, the apparatus comprising:

an impulse response estimation part for estimating an impulse response from the OFDM signal;

an integration part for integrating a signal obtained by estimation in the impulse response estimation part;

10 a determination part for detecting symbol timing of the OFDM signal based on a value obtained by integration in the integration part;

a window timing generation part for generating, according to the symbol timing, window timing to provide the valid symbol period; and

a Fourier transformation part for subjecting the OFDM signal to Fourier
15 transform according to the window timing.

As described above, in the first aspect, timing of a synchronous symbol of an incoming OFDM signal is detected so as to control window timing for Fourier transform based thereon. In this manner, in the first aspect, the OFDM signal can be demodulated, i.e., the transmission data can be reproduced with
20 between-symbol interference minimized even if a transmission path characteristic varies with time.

According to a second aspect, in the first aspect, when an identical waveform is periodically transmitted in the synchronous symbol ~~for~~ at least twice
or more, the apparatus further comprises:

25 a delay part for delaying the OFDM signal for a predetermined number of samplings;

a multiplication part for multiplying a signal obtained by delay in the delay part and the OFDM signal;

an averaging part for averaging a signal obtained by multiplication in
30 the multiplication part;

a frequency error calculation part for calculating a frequency error based on a signal obtained by averaging in the averaging part;

a hold part for holding the frequency error according to the symbol timing; and

35 a frequency correction part for correcting a frequency shift of the OFDM signal according to the frequency error provided by the hold part, wherein

the Fourier transformation part subjects, to Fourier transform, the OFDM signal with frequency shift corrected by the frequency correction part according to the window timing.

As described above, in the second aspect, in addition to the control
5 described in the first aspect, an average phase shift among the received synchronous symbols is first calculated, secondly a frequency error is calculated therefrom, and then a frequency shift of the OFDM signal is corrected. In this manner, in addition to the effects achieved in the first aspect, the symbols can be demodulated under frequency synchronization by first correcting the frequency
10 shift with the synchronous symbol used for symbol synchronization, and by Fourier-transforming the frequency-shift-corrected OFDM signal with the symbol timing.

According to a third aspect, in the first aspect,
when an identical waveform is periodically transmitted in the synchronous
15 | symbol ~~for~~ at least twice or more, the apparatus further comprising:

a first delay part for delaying the OFDM signal (hereinafter, first OFDM signal) for a first predetermined number of samplings;

a first multiplication part for multiplying a signal obtained by delay in the first delay part and the first OFDM signal;

20 a first averaging part for averaging a signal obtained by multiplication in the first multiplication part;

a first frequency error calculation part for calculating a first frequency error based on a signal obtained by averaging in the first averaging part;

25 a filter part for smoothing a signal obtained by multiplication in the first multiplication part;

an absolute value calculation part for calculating an absolute value of a signal obtained by smoothing in the filter part;

a first determination part for determining, according to the absolute
30 value, a correlation between the first OFDM signal and the signal obtained by delay in the first delay part, and detecting symbol timing of the first OFDM signal;

a first hold part for holding the first frequency error according to the symbol timing detected by the first determination part;

35 a first frequency correction part for correcting a frequency shift of the first OFDM signal according to the first frequency error provided by the first

hold part;

a second delay part for delaying, for a second predetermined number of samplings, the first OFDM signal with frequency shift corrected by the first frequency correction part (hereinafter, second OFDM signal);

5 a second multiplication part for multiplying a signal obtained by delay in the second delay part and the second OFDM signal;

a second averaging part for averaging a signal obtained by multiplication in the second multiplication part;

a second frequency error calculation part for calculating a second
10 frequency error based on a signal obtained by averaging in ~~said~~ the second averaging part;

a second hold part for holding the second frequency error according to the symbol timing detected by the determination part; and

a second frequency correction part for correcting a frequency error of
15 the second OFDM signal according to the second frequency error provided by the second hold part, wherein

the impulse response estimation part estimates the impulse response from the second OFDM signal, and

the Fourier transformation part subjects, to Fourier transform, the
20 second OFDM signal with frequency shift corrected by the second frequency conversion part according to the window timing.

As described above, in the third aspect, in addition to the control in the first aspect, frequency error correction is carried out in the first frequency corrector before calculating a correlation between the OFDM signal and the
25 synchronous symbol signal. In this manner, accuracy in detecting the synchronous symbol can be further improved. This is because a frequency-corrected signal is used for symbol synchronization and further frequency correction for the second time.

Herein, as in a fourth aspect, it is preferable that the integration part
30 in the first aspect regard a time length of the guard interval as an integration section, and integrates an incoming signal while sequentially shifting the location of the integration section with respect to the incoming signal.

Alternatively, as in a fifth aspect, the integration part in the first aspect may regard a time length of the guard interval and a predetermined time
35 length before and after the guard interval as an integration section. The integration part then integrates an incoming signal while sequentially shifting

the location of the integration section with respect to the incoming signal, and responds before and after a rectangular impulse response in the time length of the guard interval.

Alternatively, as in a sixth aspect, the integration part in the first
5 aspect may regard a time length of the guard interval and a predetermined time length before and after the guard interval as an integration section. The integration part then integrates an incoming signal while sequentially shifting the location of the integration section with respect to the incoming signal, and responds monotonously increasing before a rectangular impulse response in the
10 time length of the guard interval, but monotonously decreasing thereafter.

Preferably, as in a seventh aspect, the impulse response estimation part in the first aspect comprises:

a synchronous symbol generation part for generating a signal identical to the synchronous symbol;

15 a correlation part for calculating a signal indicating a correlation how the signal generated by the synchronous symbol generation part and the OFDM signal are correlated to each other; and

a correlation calculation part for calculating a correlation from the signal obtained by calculation in the correlation part.

20 Alternatively, as in an eighth aspect, the impulse response estimation part in the first aspect comprises:

a synchronous symbol generation part for generating a signal whose frequency domain is identical to the synchronous symbol;

25 a multiplication part for multiplying a signal provided by the Fourier transformation part and the signal provided by the synchronous symbol generation part;

an inverse Fourier transformation part for subjecting, to inverse Fourier transform, a signal obtained by multiplication in the multiplication part; and

30 a correlation calculation part for calculating a correlation from a signal provided by the inverse Fourier transformation part.

It is more preferable, as in ninth and tenth aspects, that the correlation calculation part in the seventh and eighth aspects calculates an absolute value
| of a complex vector (i, q) of the incoming signal.

Alternatively, as in eleventh and twelfth aspects, the correlation
35 calculation part in the seventh and eighth aspects calculates a sum of an absolute value of i and an absolute value of q from the complex vector (i, q) of the incoming

signal.

Alternatively, as in thirteenth and fourteenth aspects, the correlation calculation part in the seventh and eighth aspects calculates a sum of a square of i and a square of q from the complex vector (i, q) of the incoming signal.

5 More preferably, as in a fifteenth aspect, the first determination part in the third aspect receives the absolute value calculated by the absolute value calculation part, detects a value for invariability thereof, and then detects the absolute value showing a predetermined proportion to the invariable value.

As described above, in the fourth to fifteenth aspects, the timing of
10 the valid symbol period is controlled for Fourier transform to be so carried out as to minimize the between-symbol interference. This is done by first calculating a correlation between the OFDM signal and the prestored synchronous symbol signal, secondly integrating the correlation by a predetermined integral section width, and lastly, based on the integrated correlation obtained thereby,
15 detecting the synchronous symbol. In this manner, the OFDM signal can be demodulated, i.e., the transmission data can be reproduced with the between-symbol interference minimized even if a transmission path characteristic varies with time.

A sixteenth aspect of the present invention is directed to an OFDM
20 demodulation apparatus for demodulating an OFDM signal in which a known pilot carrier being a reference phase is assigned to each of a plurality of predetermined subcarriers, the apparatus comprising:

a Fourier transformation part for subjecting the OFDM signal to Fourier transform;

25 a pilot carrier extraction part for extracting the pilot carriers from a signal obtained by Fourier transform in the Fourier transformation part;

a phase change calculation part for calculating a phase change of the extracted pilot carriers;

a window shift estimation part for estimating, according to the phase
30 change, a shift of window timing indicating timing for operation of the Fourier transformation part; and

a window timing generation part for generating, according to the shift estimated in the window shift estimation part and symbol timing of the OFDM signal, the window timing which causes the Fourier transformation part to operate.

35 As described above, in the sixteenth aspect, pilot carriers are extracted from the Fourier-transformed signal, and based on a phase change of the pilot

carriers, a shift of the window timing at the time of Fourier transform is estimated and then corrected. In this manner, symbols can be demodulated under symbol synchronization even if a sampling frequency shift is observed.

A seventeenth aspect of the present invention is directed to an OFDM demodulation apparatus for demodulating an OFDM signal in which every transmission frame is provided with a predetermined reference symbol, and a known pilot carrier being a reference phase is assigned to each of a plurality of predetermined subcarriers, the apparatus comprising:

a Fourier transformation part for subjecting the OFDM signal to Fourier transform;

a reference symbol generation part for generating a signal identical to the reference symbol;

a transmission path characteristic estimation part for estimating a transmission path characteristic based on the signal generated by the reference symbol generation part and a Fourier-transformed signal in the Fourier transformation part;

an equalization part for equalizing the Fourier-transformed signal according to information about the transmission path characteristic provided by the transmission path characteristic estimation part;

a pilot carrier extraction part for extracting the pilot carriers from a signal obtained by equalization in the equalization part;

a phase change calculation part for calculating a phase change of the extracted pilot carriers;

a window shift estimation part for estimating, according to the phase change, a shift of window timing indicating timing for operation of the Fourier transformation part; and

a window timing generation part for generating, according to the shift estimated in the window shift estimation part and symbol timing of the OFDM signal, the window timing which causes the Fourier transformation part to operate.

As described above, in the seventeenth aspect, the information about transmission path characteristic is estimated from a received reference symbol, and then the OFDM signal is equalized according thereto. Further, pilot carriers are extracted from the equalized signal, and based on a phase change of the pilot carriers, a shift of the window timing at the time of Fourier transform is estimated and then adjusted. In this manner, symbols can be demodulated under symbol synchronization even if a sampling frequency shift is observed.

According to an eighteenth aspect, in the seventeenth aspect, the OFDM demodulation apparatus further comprises:

a phase shift estimation part for estimating a phase shift of the OFDM signal according to the phase change; and

5 a transmission path information correction part for correcting, according to the phase shift, the information about the transmission path characteristic to be outputted from the transmission estimation part to the equalization part.

As described above, in the eighteenth aspect, a phase change is estimated
10 | from the phase shift occurred in the symbols, and then the information about the transmission path characteristic is corrected based thereon. Accordingly, it is possible to correct a shift, due to the sampling frequency shift, between window timing at the time of Fourier transform for a reference symbol used to
| estimate the information about the transmission path characteristic and window
15 | timing at the time of Fourier transform for the OFDM signal before equalization. In this manner, accuracy in equalizing the signal is improved.

Herein, preferably, as in a nineteenth aspect, the transmission path information correction part in the eighteenth aspect corrects the information
| about the transmission path characteristic according to timing when a signal
20 | indicating the phase shift is provided by the window shift estimation part.

Alternatively, as in a twentieth aspect, the window timing generation part in the seventeenth aspect generates, according to the shift estimated in the window shift estimation part, the window timing while shifting the symbol timing for a predetermined number of samplings.

25 More preferably, as in a twenty-first aspect, in the seventeenth aspect, the OFDM demodulation apparatus further comprises:

a phase shift estimation part for estimating a phase shift of the OFDM signal according to the phase change; and

a phase correction part for correcting a phase of the signal provided
30 | by the equalization part based on the phase shift.

A twenty-second aspect of the present invention is directed to an OFDM demodulation method for demodulating an OFDM signal which includes a data symbol structured by a valid symbol period and a guard interval, and a specific synchronous symbol is included in the OFDM signal for every transmission frame
35 | ~~and~~, the method comprising the steps of:

estimating an impulse response from the OFDM signal;

integrating a signal obtained by estimation;
detecting symbol timing of the OFDM signal based on a value obtained by
integration in the integration part;
generating window timing to provide the valid symbol period based on the
5 symbol timing; and
Fourier-transforming the OFDM signal according to the window timing.
According to a twenty-third aspect, in the twenty-second aspect,
when an identical waveform is periodically transmitted in the synchronous
| symbol ~~for at least twice or more~~, the method further comprising the steps of:
10 delaying the OFDM signal for a predetermined number of samplings;
multiplying a signal obtained by delay in the delay part and the OFDM
signal;
averaging a signal obtained by multiplication in the multiplication part;
calculating a frequency error based on a signal obtained by averaging
15 in the averaging part;
holding the frequency error according to the symbol timing; and
correcting a frequency shift of the OFDM signal according to the frequency
| error provided in the holding step-, wherein
in the Fourier-transform step, the OFDM signal with frequency shift
20 corrected is subjected to Fourier transform according to the window timing.
According to a twenty-fourth aspect, in the twenty-second aspect,
when an identical waveform is periodically transmitted in the synchronous
| symbol ~~for at least twice or more~~, the method further comprising-:
a first delay step of delaying the OFDM signal (hereinafter, first OFDM
25 signal) for a first predetermined number of samplings;
a first multiplication step of multiplying a signal obtained by delay
in the first delay step and the first OFDM signal;
a first averaging step of averaging a signal obtained by multiplication
in the first multiplication step;
30 a step of calculating a first frequency error based on a signal obtained
by averaging in the first averaging step;
a step of smoothing a signal obtained by multiplication in the first
multiplication step;
a step of calculating an absolute value of a signal obtained by smoothing
35 in smoothing step;
a first determination step of determining, according to the absolute

value, a correlation between the first OFDM signal and the signal obtained by delay in the first delay step, and detecting symbol timing of the first OFDM signal;

5 a step of holding the first frequency error according to the symbol timing detected in the first determination step;

a step of correcting a frequency shift of the first OFDM signal according to the first frequency error held;

10 a second delay step of delaying, for a second predetermined number of samplings, the first OFDM signal with frequency shift corrected (hereinafter, second OFDM signal);

a second multiplication step of multiplying a signal obtained by delay in the second delay step and the second OFDM signal;

a second averaging step of averaging a signal obtained by multiplication in the second multiplication step;

15 a step of calculating a second frequency error based on a signal obtained by averaging in the second averaging step;

a step of holding the second frequency error according to the symbol timing detected in the determination step; and

20 a step of correcting a frequency shift of the second OFDM signal according to the second frequency error held, wherein

in the estimating step, an impulse response is estimated from the second OFDM signal, and

in the Fourier-transform step, according to the window timing, the second OFDM signal with frequency shift corrected is subjected to Fourier transform.

25 Herein, preferably, as in a twenty-fifth aspect, in the integrating step in the twenty-second aspect, a time length of the guard interval is ~~regard~~ regarded as an integration section, and an incoming signal is integrated while the location of the integration section being sequentially shifted with respect to the incoming signal.

30 Alternatively, as in a twenty-sixth aspect, in the integrating step in the twenty-second aspect, a time length of the guard interval and a predetermined time length before and after the guard interval are regarded as an integration section, and by integrating an incoming signal while sequentially shifting the location of the integration section with respect to the incoming signal, a
35 response is provided before and after a rectangular impulse response having the time length of the guard interval.

Alternatively, as in a twenty-seventh aspect, in the integrating step in the twenty-second aspect, a time length of the guard interval and a predetermined time length before and after the guard interval are regarded as an integration section, and by integrating an incoming signal while sequentially
5 shifting the location of the integration section with respect to the incoming signal, a response-~~which~~ monotonously increasing before a rectangular impulse response having the time length of the guard interval, but monotonously decreasing thereafter is provided.

Preferably, as in a twenty-eighth aspect, the estimating step in the
10 twenty-second aspect comprises the steps of:

generating a signal identical to the synchronous symbol;

calculating a signal indicating a correlation between a signal generated in the generating step and the OFDM signal; and

calculating a correlation from a signal obtained in the calculating step.

15 According to a twenty-ninth aspect, in the twenty-second aspect, the estimating step comprises the steps of:

generating a frequency-domain signal identical to the synchronous symbol;

20 multiplying a signal obtained in the Fourier-transform step and the frequency-domain signal generated in the generating step;

inverse-Fourier-transforming a signal obtained in the multiplying step; and

calculating a correlation from the inverse-Fourier-transformed signal.

Preferably, as in thirtieth and thirty-first aspects, in the calculating
25 step in the twenty-eighth and twenty-ninth aspects, an absolute value of a complex vector (i, q) of the incoming signal is calculated.

Alternatively, as in thirty-second and thirty-third aspects, in the calculating step in the twenty-eighth and twenty-ninth aspects, a sum of an absolute value of i and an absolute value of q is calculated from the complex
30 vector (i, q) of the incoming signal.

Alternatively, as in thirty-fourth and thirty-fifth aspects, in the calculating step in the twenty-eighth and twenty-ninth aspects, a sum of a square of i and a square of q is calculated from the complex vector (i, q) of the incoming signal.

35 More preferably, as in a thirty-sixth aspect, in the first determination step in the twenty-fourth aspect, a value for invariability of the absolute value

is detected, and then the absolute value showing a predetermined proportion to the invariable value is detected.

A thirty-seventh aspect of the present invention is directed to an OFDM demodulation method for demodulating an OFDM signal in which a known pilot carrier
5 being a reference phase is assigned to each of a plurality of predetermined subcarriers, the method comprising the steps of:

Fourier-transforming the OFDM signal;
extracting the pilot carriers from the Fourier-transformed signal;
calculating a phase change of the extracted pilot carriers;
10 estimating, according to the phase change, a shift of window timing indicating timing for Fourier transform; and
generating, according to the estimated shift and symbol timing of the OFDM signal, the window timing for Fourier transform with respect to the OFDM signal.

15 A thirty-eighth aspect of the present invention is directed to an OFDM demodulation method for demodulating an OFDM signal in which every transmission frame is provided with a predetermined reference symbol, and a known pilot carrier being a reference phase is assigned to each of a plurality of predetermined subcarriers, the method comprising the steps of:

20 Fourier-transforming the OFDM signal;
generating a signal identical to the reference symbol;
estimating a transmission path characteristic based on the generated signal and the Fourier-transformed signal;
equalizing the Fourier-transformed signal according to information about
25 the transmission path characteristic obtained in the estimating step;
extracting the pilot carriers from the equalized signal;
calculating a phase change of the extracted pilot carriers;
estimating, according to the phase change, a shift of window timing indicating timing for Fourier transform; and
30 generating, according to the estimated shift and symbol timing of the OFDM signal, the window timing for Fourier transform with respect to the OFDM signal.

According to a thirty-ninth aspect, in the thirty-eighth aspect, the method further comprises the steps of:
35 estimating a phase shift of the OFDM signal according to the phase change;
and

correcting the information about transmission path characteristic according to the phase shift.

Herein, preferably, as in a fortieth aspect, in the correcting step in the thirty-ninth aspect, the information about the transmission path characteristic is corrected according to timing when a signal indicating the phase shift is provided after being estimated in the estimating step.

Preferably, as in a forty-first aspect, in the window-timing-generating step in the thirty-eighth aspect, the window timing is generated while shifting the symbol timing for a predetermined number of timings according to the estimated shift.

More preferably, according to a forty-second aspect, in the thirty-eighth aspect, the method further comprises the steps of:

estimating a phase shift of the OFDM signal according to the phase change; and

correcting a phase of a signal provided after equalization in the equalizing step based on the phase shift.

These and other objects, features, aspects and advantages of the present invention will become more apparent from the following detailed description of the present invention when taken in conjunction with the accompanying drawings.

20

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram showing the structure of an apparatus for OFDM demodulation according to a first embodiment of the present invention;

FIG. 2 is a block diagram showing an exemplary structure of a correlator 105 in detail;

FIG. 3 is a diagram for explaining the operation of an integrator 108;

FIG. 4 is a diagram for explaining an exemplary operation of an impulse response estimator 112;

FIG. 5 is a diagram for explaining an exemplary operation of the impulse response estimator 112;

FIG. 6 is a diagram for explaining an exemplary operation of the impulse response estimator 112;

FIG. 7 is a diagram for explaining an exemplary operation of the impulse response estimator 112;

FIG. 8 is a block diagram showing the structure of an apparatus for OFDM demodulation according to a second embodiment of the present invention;

FIG. 9 is a block diagram showing the structure of an apparatus for OFDM demodulation according to a third embodiment of the present invention;

FIG. 10 is a diagram for explaining an OFDM signal applied in the third embodiment;

5 FIG. 11 is a block diagram showing the structure of an apparatus for OFDM demodulation according to a fourth embodiment of the present invention;

FIG. 12 is a diagram for explaining an OFDM signal applied in the fourth embodiment;

10 FIG. 13 is a diagram for explaining how timing of an FFT window signal is shifted by a sampling frequency shift;

FIG. 14 is a block diagram showing the structure of an apparatus for OFDM demodulation according to a fifth embodiment of the present invention;

FIG. 15 is a block diagram showing the structure of an apparatus for OFDM demodulation according to a sixth embodiment of the present invention;

15 FIG. 16 is a diagram for explaining the structure of a transmission frame applied in OFDM transmission; and

FIGS. 17A and 17B are diagrams each for explaining a signal received by a receiver when multipath occurs.

20 DESCRIPTION OF THE PREFERRED EMBODIMENTS

(First Embodiment)

By referring to FIGS. 1 to 7, ~~it is described~~ an apparatus and a method for OFDM demodulation are described according to a first embodiment of the present invention.

25 FIG. 1 is a block diagram showing the structure of the apparatus for OFDM demodulation of the first embodiment. In FIG. 1, the apparatus is provided with an A/D converter 101, an quadrature detector 102, a fast Fourier transformer 103, a data demodulator 104, and a symbol timing synchronizer 111. The symbol timing synchronizer 111 includes an impulse response estimator 112, an integrator 108,
30 a timing determination device 109, and an FFT window generator 110. The impulse response estimator 112 includes a correlator 105, a synchronous symbol generator 106, and a correlation calculator 107.

First, it is described how the apparatus for OFDM demodulation of the first embodiment operates on a constituent basis.

35 An OFDM signal transmitted from a transmitter (not shown) is received by a tuner (not shown), and then is converted into an intermediate frequency

signal arbitrarily selected by the tuner. The OFDM signal herein is similar to the one shown in FIG. 16. Herein, for the synchronous symbol, a chirp signal taking a strong autocorrelation, a signal in which a predetermined subcarrier is assigned a predetermined vector, or a signal taking an identical waveform
5 | periodically repeated ~~for at least twice or more~~ in the symbol period may be used. Herein, the synchronous symbol may be provided at the head of the respective transmission frames, or plurally provided in one transmission frame (e.g., inserted at a given interval). An advantage of a plurality of synchronous symbols is that synchronization can be established every time the synchronous
10 | symbol is detected. Accordingly, demodulation can be carried out with higher accuracy.

The OFDM signal converted into an intermediate frequency band signal is forwarded to the A/D converter 101. The A/D converter 101 converts the received OFDM signal into a time-series digital signal. The quadrature detector 102
15 | receives the digital signal from the A/D converter 101, and performs orthogonality detection thereon so as to convert the signal into a baseband signal. The baseband signal is forwarded to both the fast Fourier transformer 103 and the symbol timing synchronizer 111. The symbol timing synchronizer 111 detects symbol timing of the baseband signal, and then based on a result obtained thereby,
20 | provides the fast Fourier transformer 103 with a period during which a valid symbol is extracted (valid symbol period). According to the valid symbol period provided by the symbol timing synchronizer 111, the fast Fourier transformer 103 extracts the valid symbol from each of the transmission symbols included in the baseband signal. Thereafter, the fast Fourier transformer 103
25 | subjects the extracted valid symbols to Fourier transform so as to separate the baseband signal into subcarriers. The data demodulator 104 demodulates the signal separated into the subcarriers in the fast Fourier transformer 103 so as to reproduce the transmission data.

Next, it is described in detail how the symbol timing synchronizer 111
30 | operates.

The baseband signal converted in the quadrature detector 102 is forwarded to the correlator 105. The synchronous symbol generator 106 generates a synchronous symbol signal whose waveform is identical to that of the synchronous symbol inserted into the transmission frame on the transmitter side. Such
35 | synchronous symbol generator 106 is implemented by a memory circuit, for example. In detail, the memory circuit is previously stored with a signal whose waveform

is identical to that of the synchronous symbol inserted into the transmission frame on the transmitter side, whereby the synchronous symbol signal can be generated by reading out the stored signal. The correlator 105 receives both the baseband signal outputted from the quadrature detector 102 and the
5 synchronous symbol signal generated by the synchronous symbol generator 106. The correlator 105 then performs product-sum operation on the baseband signal and the synchronous symbol signal, and thus obtains a correlation vector.

 In ~~the~~ a case that one transmission frame has several synchronous symbols inserted at a predetermined interval, it is possible to carry out such processing
10 as described next below. The symbol timing synchronizer 111 predicts the location of the next synchronous symbol based on the timing of the detected synchronous symbol. Thereafter, the symbol timing synchronizer 111 causes the correlator 105 to operate only for a given period before and after the predicted location, and thus obtains the correlation vector.

 Herein, the correlator 105 may take the structure shown in FIG. 2. In
15 FIG. 2, the correlator 105 includes a fast Fourier transformer 703, a multiplier 701, and an inverse fast Fourier transformer 702. The baseband signal provided by the quadrature detector 102 is converted into a frequency-domain signal by the fast Fourier transformer 703. The multiplier 701 multiplies the
20 frequency-domain signal provided by the fast Fourier converter 703 and the frequency-domain synchronous symbol signal generated by the synchronous symbol generator 106 together. The signal obtained by the multiplication in the multiplier 701 is subjected to inverse Fourier transform in the inverse fast Fourier transformer 702. The inverse Fourier-transformed signal is equivalent
25 to the correlation vector between the baseband signal and the synchronous symbol signal.

 Note that, circuits of the inverse fast Fourier transformer 702 and the fast Fourier converter 703 can share the same structure. Accordingly, the fast Fourier transformer 703 may be used as an alternative to the inverse fast Fourier
30 transformer 702 to subject the signal provided by the multiplier 701 to inverse Fourier transform. It is also possible to make the fast Fourier converter 703 and the fast Fourier converter 103 sharable with each other, and the signal coming from the fast Fourier transformer 103 is forwarded to the multiplier 701. With such structure, the symbol timing synchronizer 111 can be reduced in circuit size.

35 The correlation calculator 107 receives the correlation vector obtained by the correlator 105, and then calculates the magnitude (correlation) thereof.

If the correlation vector is supposedly indicated by (i, q) , the correlation may be a sum of the square of i and that of q , an absolute value of the correlation vector as an approximate value, or a sum of an absolute value of i and that of q . The integrator 108 receives the correlation calculated by the correlation calculator 107, and then integrates the correlation. At this time, the integrator 108 regards a time length of the guard interval as an integral section width, and performs integration on an incoming signal (correlation) while sequentially shifting the integral section width. FIG. 3 shows an exemplary signal provided by the correlator 108. When received an impulse-like correlation as shown in (a) of FIG. 3, the integrator 108 performs integration thereon while sequentially shifting a given integral section width, and accordingly outputs a rectangular signal having an integral section width as shown in (b) of FIG. 3. Note that, although the integration technique applied to the integrator 108 of this embodiment is the one for outputting a rectangular response in an integral section width, it is not restrictive. Another possibility may be an integration technique for additionally responding before and after the rectangular response in the integral section width, or as shown in (c) of FIG. 3, an integration technique for having a response monotonously increasing before the rectangular response, but a response monotonously decreasing thereafter.

The correlation integrated by the integrator 108 (hereinafter, integrated correlation) is forwarded to the timing determination device 109. Based on the integrated correlation, the timing determination device 109 determines starting (or ending) timing of the synchronous symbol. Such determination is made by detecting timing when the integrated correlation becomes maximum. The starting (or ending) timing determined by the timing determination device 109 is outputted to the FFT window generator 110. Based on the starting (or ending) timing, the FFT window generator 110 generates an FFT window signal, which provides the valid symbol period in each transmission symbol to the fast Fourier transformer 103. Herein, a time length of the transmission symbol included in the transmission frame, i.e., a time length of the guard interval and that of the valid symbol are both known. Therefore, with the starting (or ending) timing of the synchronous symbol in the received signal detected by the timing determination device 109, the FFT window generator 110 can accordingly detect a head of the transmission symbols based thereon. In this manner, an FFT window signal to be generated thereby can be equivalent to the valid symbol

period.

Next, it is described in detail how the symbol timing synchronizer 111 operates with reference to FIGS. 4 to 7.

FIG. 4 shows a correlation and an integrated correlation in ~~the~~ a case that only a direct wave ((a) of FIG. 4) (without a delay wave) is provided to the apparatus for OFDM demodulation.

In this case, as shown in (b) of FIG. 4, the correlation between the direct wave and the synchronous symbol signal calculated by the correlation calculator 107 appears at the head of the direct wave. If the correlation is integrated by the integral section width which is the time length of the guard interval in the integrator 108, the integrated correlation obtained thereby is ~~in~~ rectangular as shown in (c) of FIG. 4. Accordingly, by setting, to the symbol timing, arbitrary timing within the interval A where the integrated correlation is maximum in the timing determination device 109, the apparatus for OFDM demodulation can successfully reproduce the transmission data without causing the between-symbol interference. Herein, timing at the tail of the interval A is preferably set to the symbol timing.

FIG. 5 shows a correlation and an integrated correlation in ~~the~~ a case that a combined wave ((c) of FIG. 5) of a direct wave ((a) thereof) and a delay wave ((b) thereof) is provided to the apparatus for OFDM demodulation due to multipath. FIG. 5 exemplarily shows a case that a delay of the delay wave is within the guard interval, and a power level thereof is "direct wave > delay wave". In FIG. 5, the diagonally shaded area is a part where the between-symbol interference is observed.

In this case, as shown in (d) of FIG. 5, the correlation between the combined wave and the synchronous symbol signal calculated by the correlation calculator 107 appears ~~in two~~ twice, at the head of the direct wave and at the head of the delay wave, and each is proportional to the power level thereof. If the correlation is integrated by the integral section width which is the time length of the guard interval in the integrator 108, the integrated correlation obtained thereby is as shown in (e) of FIG. 5. Therein, the interval B is the maximum. Accordingly, by setting, to the symbol timing, arbitrary timing within the interval B where the integrated correlation is maximum in the timing determination device 109, the apparatus for OFDM demodulation can successfully reproduce the transmission data without causing the between-symbol interference. Herein, timing at the tail of the interval B is preferably set to the symbol

timing.

FIG. 6 shows a correlation and an integrated correlation in ~~the~~ a case that a combined wave ((d) of FIG. 6) of a direct wave ((a) thereof) and first and second delay waves ((b) and (c) thereof) is provided to the apparatus for OFDM demodulation due to multipath. Herein, FIG. 6 exemplarily shows a case that a delay of the first delay wave is within the guard interval, but a delay of the second delay wave is not, and each power level thereof is "first delay wave > second delay wave > direct wave". In FIG. 6, the diagonally shaded area is a part where the between-symbol interference is observed.

In this case, as shown in (e) of FIG. 6, the correlation between the combined wave and the synchronous symbol signal calculated by the correlation calculator 107 appears ~~in~~ three times, at the head of the direct wave, at the head of the first delay wave, and at the head of the second delay wave, and each is proportional to the power level thereof. If the correlation is integrated by the integral section width which is the time length of the guard interval in the integrator 108, the integrated correlation obtained thereby is as shown in (f) of FIG. 6. Therein, the interval C is the maximum. As is known from (d) of FIG. 6, in the case that a delay of the delay wave (second delay wave) is beyond the guard interval, the between-symbol interference occurs no matter how the symbol timing is set. To deal with such case, in the timing determination device 109, arbitrary timing within the interval C where the integrated correlation is maximum is set to the symbol timing. In this manner, the apparatus for OFDM demodulation can successfully reproduce the transmission data while minimizing influence of the between-symbol interference. To be more specific, setting the symbol timing in the interval C results in the between-symbol interference occurring at the tail of the valid symbol period. Such between-symbol interference, however, is due to the direct wave whose power level is low, whereby the influence of the interference can be minimized.

FIG. 7 shows a correlation and an integrated correlation in ~~the~~ a case that a combined wave ((c) of FIG. 7) of a direct wave ((a) thereof) and a delay wave ((b) thereof) is provided to the apparatus for OFDM demodulation due to multipath. Herein, FIG. 7 exemplarily shows a case that a delay of the delay wave is beyond the guard interval, and a power level thereof is "direct wave > delay wave". In FIG. 7, the diagonally shaded area is a part where the between-symbol interference is observed.

In this case, as shown in (d) of FIG. 7, the correlation between the

combined wave and the synchronous symbol signal calculated by the correlation calculator 107 ~~appears in two~~ twice, at the head of the direct wave and at the head of the delay wave, and each is proportional to the power level thereof. If the correlation is integrated by the integral section width which is the time
5 length of the guard interval in the integrator 108, the integrated correlation obtained thereby is as shown in (e) of FIG. 7. Therein, the interval D is the maximum. Accordingly, by setting, to the symbol timing, arbitrary timing within the interval D where the integrated correlation is maximum in the timing determination device 109, the apparatus for OFDM demodulation can successfully
10 reproduce the transmission data while minimizing the influence of the between-symbol interference. Herein, timing at the tail of the interval D is preferably set to the symbol timing for the purpose.

It is herein assumed that the integration technique for outputting a response in a manner described in (c) of FIG. 3 is applied to the integrator 108.
15 When the above-described correlation ((d) of FIG. 7) is integrated by the integral section width in the integrator 108, an integrated correlation obtained thereby is as shown in (f) of FIG. 7. Accordingly, such integration technique makes it possible to easily obtain timing (point E) where the integrated correlation becomes maximum.

20 As described in the foregoing, according to the apparatus and method for OFDM demodulation of the first embodiment, the timing of the valid symbol period is controlled for Fourier transform to be so carried out as to minimize the between-symbol interference. This is done by first calculating a correlation between an incoming OFDM signal and a prestored synchronous symbol signal,
25 secondly integrating the correlation by a predetermined integral section width, and lastly based on the integrated correlation obtained thereby, detecting the synchronous symbol.

In this manner, with the apparatus and method for OFDM demodulation of the first embodiment, the OFDM signal can be demodulated, i. e., the transmission
30 data can be reproduced with the between-symbol interference minimized even if a transmission path characteristic varies with time.

(Second Embodiment)

By referring to FIG. 8, ~~it is described~~ an apparatus and a method for OFDM demodulation are described according to a second embodiment of the present
35 invention.

FIG. 8 is a block diagram showing the structure of an apparatus for OFDM

demodulation of the second embodiment. In FIG. 8, the apparatus is provided with the A/D converter 101, the quadrature detector 102, the fast Fourier transformer 103, the data demodulator 104, the symbol timing synchronizer 111, and a first frequency synchronizer 207. The first frequency synchronizer 207 includes a
5 delay device 201, a multiplier 202, an averaging device 203, a frequency error calculator 204, a holder 205, and a frequency corrector 206.

As shown in FIG. 8, the apparatus for OFDM demodulation of the second embodiment is additionally provided with the first frequency synchronizer 207 compared with the apparatus of the first embodiment. The first frequency
10 synchronizer 207 estimates and corrects a frequency shift among subcarriers based on a synchronous symbol to be inputted. Other constituents in the apparatus of the second embodiment are the same as those in the apparatus of the first embodiment, and are denoted by the same reference numerals and not described again.

A baseband signal converted in the quadrature detector 102 is forwarded
15 to both the delay device 201 and the multiplier 202. The delay device 201 delays the baseband signal for a predetermined number of samplings for output. The number of samplings is determined depending on each characteristic of the synchronous symbols. For instance, when the synchronous symbol is provided with the guard interval, the number of samplings for the valid symbol is set thereto.
20 The multiplier 202 multiplies the baseband signal outputted from the quadrature detector 102 by a complex conjugate of the baseband signal delayed in the delay device 201, and thus obtains a phase-difference vector (indicates how much the phase is shifted) therebetween. As described in the foregoing, a signal included in the guard interval is the one obtained by cyclically repeating a waveform of
25 the valid symbol. Accordingly, multiplying a not-delayed signal and a signal delayed for the number of samplings for the valid symbol leads to a phase-difference vector among the same waveforms. This utilizes that, when a frequency shift is observed among subcarriers, a phase is differed between a signal in the vicinity of the head and a signal in the vicinity of the tail of the transmission symbol. Accordingly, a phase-difference vector calculated by
30 the multiplier 202 tells a frequency shift (frequency error) at estimate.

Further, when the synchronous symbol is a signal taking an identical
| waveform periodically repeated ~~for at least twice or more~~ in the symbol period, the number of samplings predetermined for the delay device 201 is regarded as
35 being equal to that in a cycle of the signal. Specifically, in the case that the synchronous symbol is a signal having an identical waveform repeated for two

cycles in the symbol period, assuming that the number of samplings in a cycle is N, the signal in the delay device 201 is delayed also for the N samplings. In this manner, the multiplier 202 accordingly obtains a phase-difference among the same waveforms.

5 The averaging device 203 sequentially receives the phase-difference vector obtained by the multiplier 202, and then averages the phase-difference vectors. At this time, the averaging device 203 may calculate an average-shift of the phase-difference vectors based on the number of samplings set by the delay device 201. Thereby, the averaging device 203 obtains an average
10 phase-difference vector (phase shift on average) based on the number of samplings. Note that, by lengthening the cycle of the waveform repeated in the synchronous symbol on the transmitter side, the average phase-difference vector to be obtained by the apparatus for OFDM demodulation can be improved in accuracy. The frequency error calculator 204 is provided with the average phase-difference
15 vector obtained by the averaging device 203, and then calculates an arc tangent (\tan^{-1}) thereof so as to obtain a frequency error signal. The frequency error signal is forwarded to the holder 205. The holder 205 holds the frequency error signal with the ending timing of the synchronous symbol outputted from the timing determination device 109. The holder 205 then outputs the frequency error signal
20 on hold to the frequency corrector 206. With such hold processing, it becomes possible to utilize the frequency error signal obtained from the synchronous symbol for frequency correction of data symbols subsequent to the synchronous symbol. Based on the frequency error signal, the frequency corrector 206 performs frequency correction on the baseband signal converted by the quadrature
25 detector 102. Such frequency correction is carried out by multiplying the baseband signal by a complex SIN wave corresponding to the frequency error.

As described above, the baseband signal after the frequency correction utilizing the synchronous symbol is forwarded to the fast Fourier transformer
103 for Fourier transform, and then is demodulated in the data demodulator 104.
30 In this manner, the transmission data is reproduced in the data demodulator 104.

As is known from this, according to the apparatus and the method for OFDM demodulation of the second embodiment, in addition to the control described in the first embodiment, a frequency shift of an incoming OFDM signal can be corrected. This is implemented by averaging a phase shift among synchronous
35 symbols, and from the average phase shift obtained thereby, a frequency error is calculated for the purpose.

In this manner, with the apparatus and the method for OFDM demodulation of the second embodiment, in addition to the effects attained in the first embodiment, demodulation can be carried out with higher accuracy first by correcting a frequency shift with a synchronous symbol identical to the one used for symbol synchronization, and then subjecting an OFDM signal after the correction to Fourier transform with symbol timing.

(Third Embodiment)

By referring to FIGS. 9 and 10, ~~it is described~~ an apparatus and a method for OFDM demodulation are described according to a third embodiment of the present invention.

FIG. 9 is a block diagram showing the structure of the apparatus for OFDM demodulation of the third embodiment. In FIG. 9, the apparatus is provided with the A/D converter 101, the quadrature detector 102, the fast Fourier transformer 103, the data demodulator 104, the symbol timing synchronizer 111, the first frequency synchronizer 207, and a second frequency synchronizer 310. The second frequency synchronizer 310 includes a delay device 301, a multiplier 302, an averaging device 303, a frequency error calculator ~~204~~ 304, a holder 305, a filter 306, an absolute value calculator 307, a timing determination device 308, and a frequency corrector 309.

As shown in FIG. 9, the apparatus for OFDM demodulation of the third embodiment is additionally provided with the second frequency synchronizer 310 compared with the apparatus of the second embodiment. The second frequency synchronizer 310 estimates and corrects a frequency shift among subcarriers based on a synchronous symbol different from the one used in the first frequency synchronizer 207. Other constituents in the apparatus of the third embodiment are the same as those in the apparatuses of the first and the second embodiments, and are denoted by the same reference numerals and not described again.

FIG. 10 shows the structure of an OFDM signal to be provided to the apparatus of the third embodiment. In FIG. 10, a synchronous symbol 1 is the one used in the symbol timing synchronizer 111 and the first frequency synchronizer 207, while a synchronous symbol 2 is the one used in the second frequency synchronizer 310.

The synchronous symbol 2 may be a signal having an identical waveform periodically repeated in the symbol period. Herein, similarly to other transmission symbols, a transmission symbol in which a guard interval is provided

to a valid symbol period can be exemplarily used as the synchronous symbol 2. Preferably, in the synchronous symbol 2, the repetition cycle of the waveform in the symbol period is set to be shorter than that in the synchronous symbol 1.

5 After received such OFDM signal, the apparatus for OFDM demodulation of the third embodiment first establishes frequency synchronization by using the synchronous symbol 2, and then establishes synchronization in symbol timing and frequency by using the synchronous symbol 1. Accordingly, accuracy in detecting the synchronous symbol 1 can be further improved.

10 It is now described how the second frequency synchronizer 310 operates on a constituent basis.

A baseband signal converted in the quadrature detector 102 is forwarded to both the delay device 301 and the multiplier 302. The delay device 301 delays the baseband signal for a predetermined number of samplings for output. The
15 number of samplings is determined depending on a characteristic of the synchronous symbol 2. For instance, when the synchronous symbol 2 is provided with the guard interval, the number of samplings for the valid symbol is set thereto. The multiplier 302 multiplies the baseband signal outputted from the quadrature detector 102 by a complex conjugate of the baseband signal delayed
20 in the delay device 301, and thus obtains a phase-difference vector (indicates how much the phase is shifted) therebetween. As described in the foregoing, a signal included in the guard interval is the one obtained by cyclically repeating a waveform of the valid symbol. Accordingly, in a similar manner to the above, multiplying a not-delayed signal and a signal delayed for the number of samplings
25 for the valid symbol leads to a phase-difference vector among the same waveforms. Accordingly, a phase-difference vector calculated by the multiplier 302 tells a frequency shift (frequency error) at estimate.

Further, when the synchronous symbol 2 is a signal having an identical
30 waveform periodically repeated for at least twice or more in the symbol period, as described in the foregoing, the number of samplings predetermined for the delay device 301 is regarded as being equal to that in a cycle of the signal. In this manner, the multiplier 302 accordingly obtains a phase-difference among the same waveforms. Note that, on the transmitter side, the repetition cycle of the waveform in the symbol period in the synchronous symbol 2 may be set shorter than
35 that in the synchronous symbol 1. If that is the case, the number of samplings in the delay device 301 can be decreased in the apparatus for OFDM demodulation,

whereby the frequency error can be estimated in a quicker manner. Further, the synchronous symbols 1 and 2 may share the same waveform. If this is the case, assuming that a delay in the delay device 201 in the first frequency synchronizer 207 is equal to the number of samplings for the transmission symbol, sampling
5 is done more often to obtain the phase-difference vector. As a result, accuracy in estimating the frequency error can be further improved.

The averaging device 303 sequentially receives the phase-difference vector obtained by the multiplier 302, and then averages the phase-difference vectors. At this time, the averaging device 303 may calculate an average-shift
10 of the phase-difference vectors based on the number of samplings set by the delay device 301. Thereby, the averaging device 303 obtains an average phase-difference vector (phase shift on average) based on the number of samplings. The frequency error calculator 304 is provided with the average phase-difference vector obtained by the averaging device 303, and then calculates an arc tangent
15 (\tan^{-1}) thereof so as to obtain a frequency error signal. The frequency error signal obtained thereby in the frequency error calculator 304 is forwarded to the holder 305.

The filter 306 sequentially receives the phase-difference vector obtained by the multiplier 302 for smoothing. The absolute value calculator 307
20 receives the smoothed phase-difference vectors from the filter 306, and then determines the magnitude thereof. The timing determination device 308 receives the magnitude of the phase-difference vectors determined by the absolute value calculator 307, and according thereto, determines an ending timing of the synchronous symbol 2. Herein, the phase-difference vectors remain invariable
25 in magnitude in a period of the synchronous symbol 2 but otherwise randomly vary. Therefore, the timing determination device 308 detects whether the phase-difference vectors in a predetermined period remain invariable in magnitude. The timing determination device 308 then determines timing when the phase-difference vectors start to change in magnitude as an ending time of the
30 synchronous symbol 2. Such determination is made by first calculating a difference in magnitude between the phase-difference vectors being currently sampled and those sampled one sampling before, and then detects whether the difference is beyond a predetermined threshold value. In an alternate manner, the timing determination device 308 first detects and holds a value indicating
35 invariability of the phase-difference vectors, and then detects a time point when the phase-difference vectors start to change proportionately less than the value

(e. g., 80%).

The holder 305 holds the frequency error signal with the ending timing of the synchronous symbol 2 outputted from the timing determination device 308. The holder 305 then outputs the frequency error signal on hold to the frequency
5 corrector 309. With such hold processing, it becomes possible to utilize the frequency error signal obtained from the synchronous symbol 2 for frequency correction of the synchronous symbol 1 and data symbols subsequent to the synchronous symbol 2. Based on the frequency error signal, the frequency
10 corrector 309 performs frequency correction on the baseband signal converted by the quadrature detector 102. Such frequency correction is carried out by multiplying the baseband signal by a complex SIN wave corresponding to the frequency error.

As described above, the baseband signal after the frequency correction utilizing the synchronous symbol 2 in the second frequency synchronizer 310 is
15 forwarded to both the symbol timing synchronizer 111 and the first frequency synchronizer 207. Thereafter, the symbol timing synchronizer 111 establishes symbol synchronization utilizing data symbols frequency-corrected based on the synchronous symbol 2. The first frequency synchronizer 207 also performs frequency correction with the synchronous symbol 1 frequency-corrected based on
20 the synchronous symbol 2. The frequency-corrected baseband signal utilizing the synchronous symbol 1 is forwarded to the fast Fourier transformer 103 for Fourier transform, then is demodulated in the data demodulator 104. In this manner, the transmission data is reproduced in the data demodulator 104.

As is known from this, according to the apparatus and the method for OFDM
25 demodulation of the third embodiment, in addition to the control described in the first and second embodiments, a frequency shift is corrected in the second frequency synchronizer 310 before a correlation between an OFDM signal and a synchronous symbol signal is calculated.

In this manner, with the apparatus and the method for OFDM demodulation
30 of the third embodiment, in addition to the effects attained in the first and second embodiments, accuracy in detecting the synchronous symbol 1 can be further improved. This is because a frequency-corrected signal utilizing the synchronous symbol 2 is used for symbol synchronization and frequency correction. Further, higher-speed frequency correction is implemented by using, as the
35 synchronous symbol 2, a signal whose repetition cycle of a waveform is shorter.

(Fourth Embodiment)

By referring to FIGS. 11 to 13, ~~it is described~~ an apparatus and a method for OFDM demodulation are described according to a fourth embodiment of the present invention.

5 FIG. 11 is a block diagram showing the structure of the apparatus for OFDM demodulation of the fourth embodiment. In FIG. 11, the apparatus is provided with the A/D converter 101, the quadrature detector 102, the fast Fourier transformer 103, the data demodulator 104, the symbol timing synchronizer 111, an equalizer 801, a transmission path estimator 802, a reference symbol generator
10 803, a pilot carrier (PC) extractor 804, a phase change calculator 805, and a window shift estimator 806.

As shown in FIG. 11, the apparatus for OFDM demodulation of the fourth embodiment is additionally provided with the equalizer 801, the transmission path estimator 802, the reference symbol generator 803, the PC extractor 804, the phase
15 change calculator 805, and the window shift estimator 806 compared with the apparatus of the first embodiment. Other constituents in the apparatus of the fourth embodiment are the same as those in the apparatus of the first embodiment, and are denoted by the same reference numerals and not described again.

FIG. 12 shows the structure of an OFDM signal to be inputted to the
20 apparatus of the fourth embodiment. A synchronous symbol in FIG. 12 is the one used in the symbol timing synchronizer 111 and the first frequency synchronizer 207. Symbol timing detected by the synchronous symbol is a basis for demodulation to be carried out for symbols subsequent to the synchronous symbol. A known reference symbol is used to estimate information about transmission path
25 characteristic. Based on the estimation, symbols subsequent to the reference symbol are equalized. Herein, since any known symbol may be the reference symbol, the synchronous symbol may be used as the reference symbol.

Next below, it is described in detail the operation of the equalizer 801, the transmission path estimator 802, the reference symbol generator 803, the PC
30 extractor 804, the phase change calculator 805, and the window shift estimator 806. Based on the symbol timing signal detected by utilizing the synchronous symbol, the FFT window generator 110 generates an FFT window signal, which provides the fast Fourier transformer 103 with timing to operate. The fast Fourier transformer 103 subjects, based on the FFT window signal, the received
35 baseband signal to Fourier transform in the valid symbol period to obtain a frequency-domain signal. Symbols converted into the frequency-domain signal

are forwarded to both the equalizer 801 and the transmission path estimator 802.

The reference symbol generator 803 generates a frequency-domain signal $X_{ref}(k)$ of the known reference symbol. The reference symbol generator 803 can be implemented by a memory circuit, for example. In detail, the memory circuit
5 is previously stored with a signal whose waveform is identical to that of the reference symbol inserted into the transmission frame on the transmitter side, whereby the signal $X_{ref}(k)$ can be generated by reading out the stored signal.

Based on the reference symbol signal, the transmission path estimator 802 estimates, under a technique next below, the information about transmission
10 path characteristic indicating an impulse response on the transmission path.

Assuming that a transmitting signal from the transmitter is $x(t)$, a received signal received by the receiver is $r(t)$, and a transmission path characteristic between the transmitter and the receiver is $h(t)$, the following equation is established thereamong:

15
$$R(k) = H(k) \times X(k)$$

where $R(k)$, $H(k)$, and $X(k)$ are Fourier-transformed values of $r(t)$, $h(t)$, and $x(t)$.

Therefore, with the transmitting signal $X(k)$ being known, the transmission path characteristic $H(k)$ can be estimated by the following equation:

20
$$H(k) = R(k) / X(k)$$

Accordingly, the transmission path estimator 802 divides a reference symbol signal $R_{ref}(k)$ of the received signal by the signal $X_{ref}(k)$ generated by the reference symbol generator 803 so as to estimate the transmission path characteristic $H(k)$.

25 Assuming that a transmitting signal of a data symbol from the transmitter is $X_{data}(k)$, a received signal of a data symbol received by the apparatus for OFDM demodulation is $R_{data}(k)$, and a transmission path characteristic between the transmitter and the apparatus is $H'(k)$, the following equation is established thereamong:

30
$$R_{data}(k) = H'(k) \times X_{data}(k)$$

Herein, if the transmission path characteristic $H(k)$ estimated from the reference symbol is approximately equal to the transmission path characteristic $H'(k)$ of the data symbol, the transmitting signal of the data symbol $X_{data}(k)$ may be reproduced by dividing the received signal of the data symbol $R_{data}(k)$
35 by the transmission path characteristic $H(k)$.

Therefore, the equalizer 801 equalizes the OFDM signal by dividing the

received signal of the data symbol $R_{data}(k)$ by the transmission path characteristic $H(k)$ estimated by the transmission path estimator 802.

In the case that any sampling frequency shift is observed between the transmitter and the receiver, the symbol timing may be also shifted. It is described in detail by referring to FIG. 13. In FIG. 13, a transmission symbol includes a guard interval having a number of samplings L and a valid symbol period having a number of samplings M . A plurality of transmission symbols structure a transmission frame.

On the receiver side, a synchronous symbol is detected so as to generate an FFT window signal indicating the valid symbol period in the respective transmission symbols. Herein, the number of samplings L and M are both known. Therefore, starting with detection timing of the synchronous symbol, the FFT window signal generated on the receiver side has a Low-level waveform repeated in a period for the number of samplings L , but a High-level waveform in a period for the number of samplings M .

If the sampling frequency is shifted between the transmitter and the receiver, a time length T for one sampling on the transmitter side and a time length T' therefor on the receiver side show a difference therebetween. Accordingly, even if the number of samplings M is the same, the time length for M samplings on the transmitter side is $(M \times T)$, but on the receiver side $(M \times T')$, and thus are differed. Consequently, a shift starts to be generated between the valid symbol period of the received signal and the period in which the FFT window signal is in High level. The shift is accumulated and thus becomes large for data symbols toward the tail of the transmission frame.

With the timing of the FFT window signal shifted, the frequency-domain signal may have phase shift in the symbols. At this time, the received signal $R(k)$ is expressed as follows:

$$R(k) = H(k) \times X(K) \times \exp(-j \times 2\pi \times k \times \Delta t / N)$$

where N is the number of FFT points (the number of samplings in the valid symbol period), k is a subcarrier frequency, and Δt is a timing shift of the FFT window signal.

As is known from this, even if the received signal $R(k)$ is equalized by dividing the same by the transmission path characteristic $H(k)$ estimated by the reference symbol, a term of the phase shift caused by the timing shift Δt of the FFT window signal cannot be deleted from the above equation. Provided that the received signal after the equalization is $R'(k)$, the following equation

is established:

$$R'(k) = X(k) \times \exp(-j \times 2\pi \times k \times \Delta t / N)$$

Therefore, in the transmitter, a plurality of predetermined subcarriers in the transmission symbols are each assigned a known pilot carrier as a reference phase. These subcarriers k ($k = k_0, k_1, \dots, k_n$) in the transmitting signal $X(k)$ are supposedly known. The pilot carriers are each exemplarily assigned to subcarriers k at regular frequency intervals, or may be assigned to the subcarriers k at predetermined irregular intervals. Herein, the predetermined irregular interval may be defined by PN series.

Since the pilot carriers $X(k)$ for transmission are known, a phase difference $\Phi(k)$ from pilot carriers $R'(k)$ for reception is calculated as follows:

$$\Phi(k) = -2\pi \times k \times \Delta t / N$$

As is known from this, finding a change in the phase difference $\Phi(k)$ with respect to the subcarriers k lead to the timing shift Δt of the FFT window signal.

Thereafter, the PC extractor 804 extracts pilot carriers from the received signal equalized by the equalizer 801. The extracted pilot carriers are outputted to the phase change calculator 805. The phase change calculator 805 calculates a phase of the pilot carriers and then a phase change with respect to the subcarriers k . The phase change obtained thereby is supplied to the window shift estimator 806. Based on the phase change with respect to the subcarriers k in the transmission symbols, the window shift estimator 806 estimates the timing shift Δt of the FFT window signal. The estimated timing shift Δt is provided to the FFT window generator 110. The FFT window generator 110 adjusts timing of generating the FFT window signal based on the timing shift Δt .

As described above, according to the apparatus and method for OFDM demodulation of the fourth embodiment, in addition to the control described in the first embodiment, an OFDM signal is equalized after a transmission path is estimated from a received reference symbol. Further, pilot carriers are extracted from the equalized signal, and based on a phase change of the pilot carriers, a timing shift of the FFT window signal at the time of Fourier transform is estimated and then corrected.

In this manner, with the apparatus and method for OFDM demodulation of the fourth embodiment, in addition to the effects attained in the first embodiment, symbols can be demodulated under symbol synchronization even if a sampling frequency shift is observed.

(Fifth Embodiment)

Alternatively, any influence of the timing shift Δt of the FFT window signal may be corrected as described next below in a fifth embodiment. By
5 referring to FIG. 14, it is described an apparatus and a method for OFDM demodulation of the fifth embodiment.

FIG. 14 is a block diagram showing the structure of the apparatus for OFDM demodulation of the fifth embodiment. In FIG. 14, the apparatus of the fifth embodiment is additionally provided with a transmission path information
10 corrector 901 and a phase shift estimator 902 compared with the apparatus of the fourth embodiment.

The phase change estimator 805 calculates, similarly to the fourth embodiment, the phase difference $\Phi(k)$ with respect to the subcarriers k as follows:

15
$$\Phi(k) = -2\pi \times k \times \Delta t / N$$

With the phase difference $\Phi(k)$, the phase shift estimator 902 estimates a phase shift $P(k)$ through the following calculation:

$$P(k) = \exp(\Phi(k)) \exp(-j \times 2\pi \times k \times \Delta t / N)$$

The transmission path information corrector 901 multiplies the phase
20 shift $P(k)$ and the above-estimated information about transmission path characteristic $H(k)$ so as to obtain the corrected information about transmission path characteristic $H_p(k)$.

$$H_p(k) = H(k) \times P(k)$$

The equalizer 801 divides the received signal $R(k)$ by the corrected
25 information about transmission path characteristic $H_p(k)$ so as to equalize the received signal $R(k)$ ($=R'(k)$).

$$R(k) = H(k) \times X(k) \times \exp(-j \times 2\pi \times k \times \Delta t / N)$$

$$R'(k) = R(k) / H_p(k) = X(k)$$

Alternatively, the information about transmission path characteristic
30 $H(k)$ is corrected by the phase shift $P(k)$ caused by the estimated timing shift Δt , and with the corrected information about transmission path characteristic $H_p(k)$, the received signal $R(k)$ may be equalized.

If the estimated timing shift Δt is closer to the sampling frequency,
such operation as described next below may be possible. The FFT window generator
35 110 adjusts the timing of generating the FFT window signal. When the timing shift Δt is smaller than the sampling frequency, the transmission path information

corrector 901 corrects the information about transmission path characteristic so that the phase shift caused by the timing shift Δt is corrected. With the timing of generating the FFT window signal adjusted by the FFT window generator 110, the window shift estimator 806 causes the transmission path information
5 corrector 901 to operate based on the adjusted timing, and then the transmission path information corrector 901 may correct the phase shift caused by the timing change of the FFT window signal.

As is obvious from this, demodulation can be carried out with higher accuracy by demodulating the received signal $R'(k)$ equalized in the equalizer
10 801 in the data demodulator 104.

(Sixth Embodiment)

Alternatively, any influence of the timing shift Δt of the FFT window signal may be corrected as described next below in a sixth embodiment. By
15 referring to FIG. 15, ~~it is described~~ an apparatus and a method for OFDM demodulation are described of the sixth embodiment.

FIG. 15 is a block diagram showing the structure of the apparatus for OFDM demodulation of the sixth embodiment. In FIG. 15, the apparatus of the sixth embodiment is additionally provided with the phase shift estimator 902 and a phase
20 corrector 903 compared with the apparatus of the fourth embodiment.

The phase change estimator 805 calculates, similarly to the fourth embodiment, the phase difference $\Phi(k)$ with respect to the subcarriers k as follows:

$$\Phi(k) = -2\pi \times k \times \Delta t / N$$

25 With the phase difference $\Phi(k)$, the phase shift estimator 902 estimates a phase shift $P(k)$ through the following calculation:

$$P(k) = \exp(\Phi(k)) = \exp(-j \times 2\pi \times k \times \Delta t / N)$$

The equalizer 801 divides the received signal $R(k)$ by the information about transmission path characteristic $H(k)$ so as to equalize the received signal
30 $R(k)$ ($=R'(k)$).

$$R(k) = H(k) \times X(k) \times \exp(-j \times 2\pi \times k \times \Delta t / N)$$

$$R'(k) = R(k) / H(k)$$

$$= X(k) \times \exp(-j \times 2\pi \times k \times \Delta t / N)$$

The phase corrector 903 inversely shifts, by the phase shift $P(k)$, the
35 received signal R' equalized by the equalizer 801 so as to correct the phase. In order to inversely shift the phase, the received signal $R'(k)$ is multiplied

by a complex conjugate of the phase shift $P(k)$.

$$R''(k) = R'(k) \times \exp(j \times 2\pi \times k \times \Delta t / N) = X(k)$$

As is known from this, demodulation can be carried out with higher accuracy by demodulating the received signal $R''(k)$, which is equalized in the equalizer 801 and then phase-corrected in the phase corrector 903, in the data demodulator 104.

Note that, the apparatuses in the fourth to sixth embodiments are structured by adding, to the apparatus for OFDM demodulation of the first embodiment, the equalizer 801, the transmission path estimator 802, the reference symbol generator 803, the PC extractor 804, the phase change calculator 805, the window shift estimator 806, the transmission path information corrector 901, the phase shift estimator 902, and the phase corrector 903. Such structures are not restrictive, and the above-described constituents can be added to the apparatuses of the second and third embodiments.

Further, the calculation processing carried out in the apparatuses of first to sixth embodiments can be implemented by a digital signal processor (DSP), for example. Still further, such calculation processing can be implemented by carrying out a program recorded on a recording medium as a computer program for steps of the calculation processing.

While the invention has been described in detail, the foregoing description is in all aspects illustrative and not restrictive. It is understood that numerous other modifications and variations can be devised without departing from the scope of the invention.

ABSTRACT OF THE DISCLOSURE

5 ~~An object of the present invention is to provide an~~ An apparatus and method
for OFDM demodulation ~~establishing~~ establish symbol synchronization ~~in such a~~
~~manner as to minimize between-symbol interference even under the~~ an environment
where multipath occurs, ~~and a method therefor.~~

10 An incoming signal is an OFDM signal including a transmission symbol
structured by a valid symbol period and a guard interval, and a predetermined
synchronization symbol is included in the OFDM signal for every transmission
frame, ~~and.~~ A correlator ~~105~~ calculates how a signal generated by a
synchronization symbol generator ~~106~~ and the OFDM signal are correlated ~~to each~~
~~other.~~ A correlation calculator ~~107~~ then calculates a correlation therefrom.
An integrator ~~108~~ integrates the calculated correlation by the guard interval.
A timing determination device ~~109~~ determines symbol timing from the integrated
15 correlation. An FFT window generator ~~110~~ outputs operation timing for Fourier
transform from the determined symbol timing. Based on the signal outputted from
the FFT window generator ~~110~~, the apparatus for OFDM demodulation extracts a
signal in the valid symbol period from the transmission symbol for demodulation.